

2020 IEEE Custom Integrated Circuits Conference (CICC) – Program-at-a-Glance

	<i>Harborside Ballroom</i>	<i>Aquitania</i>	<i>Michelangelo</i>	<i>Olympia</i>	<i>Raffaello</i>
Sunday, March 22 – Registration 7:30 am-5:00 pm – <i>Grand Foyer</i> *All Sunday sessions are included with conference registration*					
9:00 am-4:45 pm	Educational Session 1: RF/mmW Power Amplifiers	Educational Session 2: Phase-Locked Loops	Educational Session 3: Transceivers (5G /IoT)	Educational Session 4: Data Converters and Interface Circuits	MathWorks Sessions (9:00 am-12:15 pm)
Monday, March 23 – Registration 8:00 am-5:00 pm – <i>Grand Foyer</i>					
8:30 am-8:50 am	Welcome and Opening Remarks – Grand Ballroom				
8:50 am-9:40 am	Session 1: Keynote Session – Grand Ballroom				
9:40 am-10:00 am	Break – Grand Foyer				
10:00 am - 12:10 pm	Session 2: Trust: Innovations in Security Circuits	Session 3: PLL and VCO Techniques	Session 4: Forum: Electronics for Vehicles of the Future	Session 5: System Oriented Design Foundations	
12:10 pm- 2:00 pm	Keynote Luncheon – Skyline Ballroom				
2:00 pm-3:45 pm (Break 3:45 pm-4:00 pm)	Session 6: Emerging Devices and Sensing Applications	Session 7: RF & mm-WAVE Circuits and Transceivers	Session 8: Analog Circuits and Systems	Session 9: Forum: IP Blocks: Challenges for Next Decade Complex SoCs	
4:00 pm-5:15 pm				Session 10: Panel: Are Figures of Merit (FoMs) Killing Creativity?	
5:30 pm - 7:00 pm	Welcome Reception- Skyline Ballroom				
Tuesday, March 24 – Registration 8:00 am-5:00 pm – <i>Grand Foyer</i>					
8:00 am-8:50 am	Session 11: Keynote Session - Grand Ballroom				
8:50 am-9:10 am	Break – Grand Foyer				
9:10 am-11:45 am	Session 12: Data Converter Techniques	Session 13: Efficient Neural Network Acceleration	Session 14: DC-DC Converters	Session 15: Panel: Will THz Applications catch up with THz Technology?	
11:45 am- 1:30 pm	Lunch Break (on your own)				
1:30 pm-5:40 pm (Break 3:15 pm-3:30 pm)	Session 16: Energy-Efficient Systems for Wearable, Implantable, and IoT Applications	Session 17: SoC Design: From Bits to Gigabits	Session 18: Ultra-low Power Wireless Transceivers	Session 20: Forum: Journey of a Startup	
3:30 pm-5:40 pm			Session 19: High-Speed Wireline Transceivers	Session 21: Forum: Advances in 5G and Wireless Systems	
6:00 pm-8:00 pm	Reception – Skyline Ballroom				
Wednesday, March 25 - Registration 8:00 am-11:00 am – <i>Grand Foyer</i>					
8:30 am-9:20 am	Session 22: Keynote Session - Grand Ballroom				
9:20 am-9:40 am	Break - Grand Foyer				
9:40 am-12:15 pm	Session 23: Oversampled Data Converters	Session 24: GaN and High-Voltage Power Converters	Session 25: Analog in Advanced Technologies	Session 26: Technology Oriented Design Foundations	
12:15 pm-1:30 pm	Lunch Break (on your own)				
1:30 pm-3:30 pm	Session 27: Panel: Who is Leading Innovations, Academia or Industry?	Best Papers Poster Session - Grand Ballroom			
3:30 pm-4:00 pm	Closing and Awards Ceremony – Grand Ballroom				