

Conference Overview

	Salon D	Salon E	Room 408	Room 410	Foyer
Sunday, April 14					
8:30 - 4:30	Educational Session 1 Power Management Fundamentals	Educational Session 4 Machine Learning, Quantum, and Security Hardware	Educational Session 2 What You Need to Know to Design Effectively in 10 nm and Beyond	Educational Session 3 56/112G Link Foundations	Registration 7:30 am - 5:00 pm
Monday, April 15					
8:00 - 10:00	Session 1 - Plenary in Salon C				Registration 7:30 - 5:00
10:30 - 12:15	Session 2 Millimeter-Wave and Sub-Terahertz Transceivers	Session 3 - Oscillators and PLLs	Session 4 Advanced Accelerators and Digital Design Techniques	Session 5 Forum-New Paradigm in Miniaturized Electronics- Wearables and Neural Interfaces	
1:30 - 5:30	Session 6 RF Transmitters and Building Blocks	Session 7 Sensor and Interface Circuits	Session 8 Wireline Clocking Techniques	Session 9 DC DC converters and Digital LDO's	
1:30 - 3:15					
3:30 - 5:30			Session 10 - Advanced Frequency Generator		
5:30 - 7:00					Reception - Salon C
Tuesday, April 16					
8:30 - 12:00		Session 13 High Speed ADCs	Session 14 Biomedical Circuits and Systems	Session 15 Security, Memory and Computing	Registration 8:00 am - 5:00 pm
8:30 - 10:15	Session 12 Advanced RF Techniques				
10:40 - 12:00	Session 16 - Panel-Choices (and Consequences) for Neural Network HW and Systems				
12:20 - 1:45					CICC Luncheon - Salon
2:00 - 5:30	Session 17 Modeling, Reliability and Safety	Session 18 Innovative Wireline Techniques		Session 19 Heterogeneous Integration and Chip-to-Chip	
2:00 - 3:45			Session 14 Biomedical Circuits and Systems		
4:00 - 5:30			Session 20 Panel - Research Direction Mismatches Between		
4:30 - 6:00					Young Professionals and WIC Mentoring Event - Room 406
5:30 - 7:00					Reception - Salon C
Wednesday, April 17					
8:30 - 12:00	Session 21 Data Converter Techniques	Session 22 Sensors and Integrations	Session 23 - Emerging Power Management Techniques and Energy Harvesting		Registration 8:00 am - 11:00 am
8:30 - 10:15				Session 24 - Forum-Alternative computing Models using Analog/MS Computational Substrates	
10:35 - 12:15				Session 25 - Forum-Designing Secure HW	
1:30 - 4:05	Session 26 RF/mmW VCOs and Mixers	Session 27 Circuits and Systems for Security			
2:30 - 4:30				Best Paper Poster Display	
4:30 - 4:50				Closing and Awards Ceremony	