

Conference Overview

	GREAT ROOM 1-3	GREAT ROOM 4	GREAT ROOM 5	GREAT ROOM 6-8	GALLERY
Sunday, April 8					
8:30 - 4:30	Educational Session 1 Data Converter Circuits	Educational Session 2 Clocking Circuits	Educational Session 3 Millimeter-Wave Circuits	Educational Session 4 Low Power IOT	Registration 7:30 am - 5:00 pm
Monday, April 9					
8:00 - 9:30		Session 1 - Plenary			Registration 7:30 - 5:00
10:00 - 12:00	Session 2 Wireline Techniques for Advanced Modulation Schemes	Session 3 - Forum -Self Driving Car Tech. and Associated Computational Power Requirements	Session 4 Advanced RF Transceivers	Session 5 Advanced Analog Techniques	
1:30 - 5:30	Session 6 Power Management Circuits and Architectures	Session 7 Sensor Interface Techniques	Session 8 Human Body Communications and Emerging Applications	Session 9 High Performance Oscillators and Low-Power Digital Clock Generation	
1:30 - 3:15				Session 10 - Forum -The Next Waves of Machine and Deep Learning Hardware	
3:30 - 5:30					
5:30 - 7:00					Reception
Tuesday, April 10					
8:30 - 12:00	Session 11 CMOS Biochips and Bioelectronics	Session 12 - Forum -The Vanishing Boundary Between Digital and Analog	Session 13 THz, mmWave and RF Techniques	Session 14 Nyquist ADC	Registration 8:00 am - 5:00 pm
12:20 - 1:45					CICC Luncheon
2:00 - 5:30	Session 15 Design Foundations for Advanced Technologies	Session 16 Cognitive Memories and Novel Accelerators	Session 17	Session 18 Wireline Transceivers and Building Blocks	
2:00 - 3:45			Panel-What is the Sweet Spot of Voltage Regulator Integ		
4:00 - 5:30			Session 19 Power Management Techniques Energy Harvesting		
5:30 - 7:00					
Wednesday, April 11					
8:30 - 12:00	Session 20 Oversampling AID Converters	Session 21 Timing Techniques			Registration 8:00 am - 11:00 am
8:30 - 10:15			Session 22 - Forum - Device and Integration at Advanced Technology Nodes	Session 23 - Panel - Bio-inspired Learning & Inference Systems: What Works & What Didn't	
11:00 - 12:15			Session 24 - Panel -Is the IC Startup Era Over or Just Transitioning	Session 27 - Panel - What can/should Circuit Designers to to Ride on the Wave of Machine Learning	
1:30 - 2:30					Best Paper Awards Ceremony