

CONFERENCE OVERVIEW							
	OAK BALLROOM	FIR BALLROOM	PINE BALLROOM	CEDAR BALLROOM	SILICON VALLEY ROOM	BAYSHORE FOYER	DONNER, SISKIYOU, BALLROOMS
SUNDAY, SEPTEMBER 27							
2:00 pm - 5:00 pm						Technical Session Registration	
MONDAY, SEPTEMBER 28							
7:30 am - 5:00 pm						Technical Session Registration 7:30 am - 5:00 pm	Exhibits Open 4:00 – 8:00 pm
8:15 am - 9:30 am	1. Keynote Presentation						
10:00 am - 12:00 pm	2. Low Power	3. Optical Interconnect and Reliability Enhancement Techniques	4. Frequency and Phase Generation Techniques	Ed Session 1-Low Dropout Regulators			
1:30 pm – 3:15 pm Session 8 starts at 1:00 pm	5. Panel Discussion-Impedance Mismatch between Academia and Industry	6. Analog Circuits Using Digital Cells	7. Advances in Biomedical Sensor Systems	Ed Session 2 – Behavioral Modeling Options for balancing verification coverage and Credibility	8. Forum-Silicon Photonics Opportunities and Challenges		
3:30 pm – 5:30 pm	9. Advanced Simulation Techniques	10. Memory Advancements	11. Advanced Techniques for Power Amplifier Transceiver Front-ends	12. Tutorial – Beyond CMOS: Large Area Electronics-Concepts and Prospects	13. Panel – What is 5G		
5:00 pm - 7:00 pm							Poster Session
5:30 pm - 8:00 pm							Reception
TUESDAY, SEPTEMBER 29							
8:30 am – 5:00 pm							
9:00 am - 12:00 pm	14. Emerging Technology, Power and Cooling	15. Wireline Techniques	16. Devices Scaling and Design Interaction in Advanced Nodes	Ed Session 3 - Multiphase RF Techniques in CMOS Applied to Beam-forming and Full Duplex Receivers Ed Session 4 – Phase-Locked Frequency Synthesis and Modulation for Modern wireless Transceivers	17. Forum-Wireless Power for Biomedical Applications	Technical Session Registration 8:30 am - 5:00 pm	Exhibits Open 4:00 – 8:00 pm
12:20 pm – 1:50 pm	SIERRA BALLROOM – CICC LUNCHEON						
2:00 pm –5:00 pm	18. Data Converter Techniques	19. Power Management	20. Manufacturing Beyond Moore's Law	Ed Session 5 – Supply Noise Induced Jitter Modeling and Optimization for High-Speed Interfaces Educational Session 6 – MMIC for 5G	21. Panel – Semiconductor Startups in the New Millennium		
5:00 pm – 7:00 pm							Poster Session
5:30 pm – 8:00 pm							Reception
WEDNESDAY, SEPTEMBER 30							
9:00 am - 12:00 pm	22. High Frequency Analog Techniques 25. 20 Gb/s Transmitters and Receivers	23. Modeling Emerging Devices	24. 5G mm-wave and Next Generation Wireless Systems	Educational Session 7 – SAR ADCs in Time-Interleaved Converter Arrays		Technical Session Registration 8:30 am - 11:00 am	
1:30 pm – 5:00 pm	26. Forum – Advanced Power Amplifier Techniques for Mobile Devices			Educational Session 8 – Resonant Wireless Power Transfer: Technology and Integration Roadmap			